Announcement for DAC 2019

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VERITOOLS ANNOUNCES FOR DAC 2019

Veritools ports its world class software tools to the PC. Since every engineer has a PC on their desk, this PC in native windows mode can now be used for most of their design and debugging efforts

The exact same software that runs on Linux, now also runs on native Windows. These tools include:

vWave-Univ (Undertow)

EDA's fastest waveform viewer with total and complete features for both digital and analog designers

VeritoolsDesigner-Univ

Source code debugger with State diagrams and RTL/Gate Schematics, for Verilog, SystemVerilog and VHDL

VeritoolsVerifyer-Univ

EDA's only standalone SystemVerilog Assertion simulator/debugger/analyzer, speeds SVA debugging 100X

Veritools is a leading provider of waveform display, source code debugging and SVAssertions tools for the digital and mixed integrated circuits markets. Veritools software has been used by several thousands of design and verification engineers at over 350 companies in 45 countries.

Customer interest and sign up for the following prizes:

Amazon Dot, one per day, at 5:30 PM

Apple Air-pad, on Wednesday at 5:30 PM

Bring this paper by our DAC booth in order to enter the Veritools drawing for several 11-inch Laptops, one given away at end of each day of conference. Please write legibly.

Name		_	
Email Address	S		
Company			
Phone Number	er		
Indicate and C	heck off Areas of interest		
☐ Analog		☐ RTL Source Code Debugging	□ VHDL
☐ Digital	☐ Fast Waveform display	☐ SVAssertion	□ Verilog/SystemVerilog

Veritools is a leading provider of waveform display, source code debugging and SVA assertions tools for the digital and mixed integrated circuits markets. Veritools software has been used in over 350 companies in 45 countries. For more information please visit our DAC booth, email inquiry@veritools.com, or schop@earthlink.net, or call 650 533-5595 or visit http://www.veritools.com.